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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	09/970,351
				Filing Date	October 3, 2001
				First Named Inventor	Carey Ritchey
				Art Unit	2681
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	49581-P030US-10104106

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
SJ 1016	A	6,285,865	09-04-2001	Vorenkamp et al.	C1/SUBC/20 455/507

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See attached Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the application number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Examiner Signature		Date Considered	06/05/02 4/7/03
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U.S. PATENT DOCUMENTS					
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		Number-Kind Code <sup>2</sup> (if known)			
SJ	AA	4,590,417	05-20-86	Tanaami et al.	6/25/86-8/25/86 323/229

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SJ	CA	"A Wideband General purpose PIN Diode Attenuator," pages 1-11, APN1003, Copyright Alpha Industries, Inc., 1999.	
SJ	GB	"A Low-Cost Surface Mount PIN Diode $\pi$ Attenuator," Hewlett-Packard Application Note 1048, pages 2-29 through 2-34, 5966-0149E. No Date - Can't Consider	
SJ	CC	Addendum to Hewlett-Packard Application Note 1048 "A Low-Cost Surface Mount PIN Diode $\pi$ Attenuator," R. Waugh, Rev. 2/5/96, 4 pages.	
SJ	CD	"Applications of PIN Diodes," Hewlett-Packard, Application Note 922, pages 2-9 through 2-24, 5965-8666E. No Date - Cannot Consider	
SJ	CE	"Low Distortion Attenuator Plastic Packaged PIN Diodes," Alpha Industries, Inc. SMP1304 Series, pages 1-4, 6/99.	
SJ	CF	"Linear Models for Diode Surface Mount Packages," Hewlett-Packard Application Note 1124, copyright 1997, Hewlett-Packard Co., 5966-0399E(8/97).	
SJ	CG	"Surface Mount PIN Diodes," Hewlett-Packard Technical Data, HSMP-38XX and HSMP-48XX Series, pages 1-12, Copyright 1999 Hewlett-Packard Co., Obsolete 5968-3435E (6/99)	
SJ	CH	"A CATV Attenuator Using the Single Package SMP1307-027 PIN Diode Array," Alpha Industries, Inc. APN1017, pages 1-9, 11/99.	
SJ	CI	Chapter 3, "Pin Diode RF Attenuators," "Pin Diode Variable Attenuators," Microsemi Corp., pages 1-13. No Date - Cannot Consider	
SJ	CJ	"Surface Mount RF PIN Low Distortion Attenuator Diodes," Agilent Technologies, Technical Data, HSMP-381xSeries and HSMP-481xSeries, pages 1-8, Copyright 1999 Agilent Technologies, Obsolete 5091-4932E (11/99).	
SJ	CK	"A Low Distortion PIN Diode Switch Using Surface Mount Devices," Agilent Technologies, Application Note 1049, pages 1-10, Copyright 1999 Agilent Technologies, Obsolete 5091-4932E, 5966-0780E (11/99).	

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